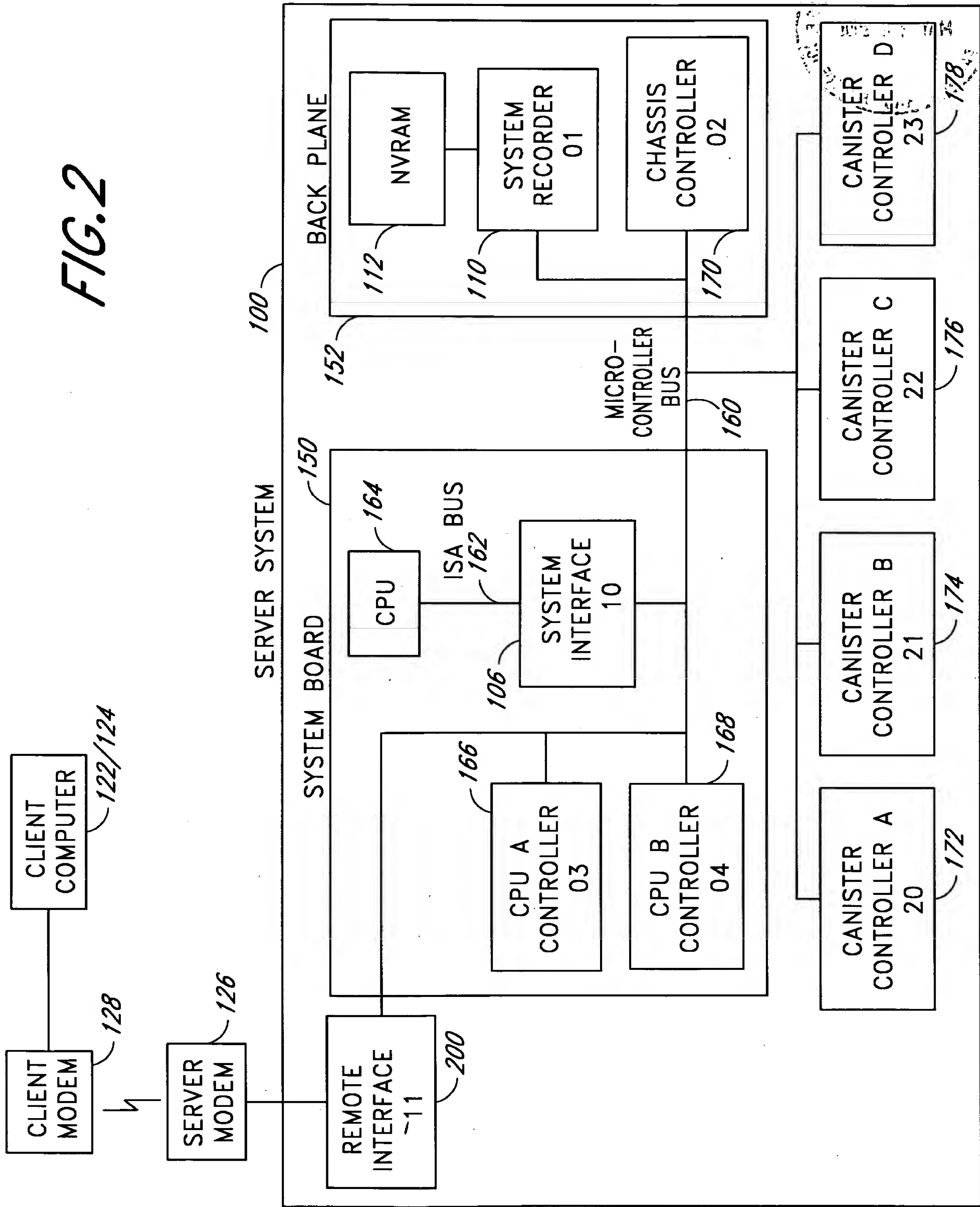


FIG. 1

FIG. 2



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# REMOTE INTERFACE SERIAL PROTOCOL MESSAGE FORMATS

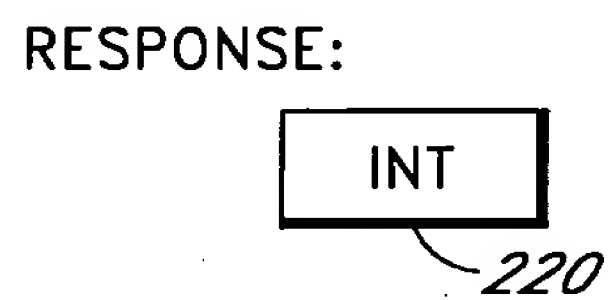
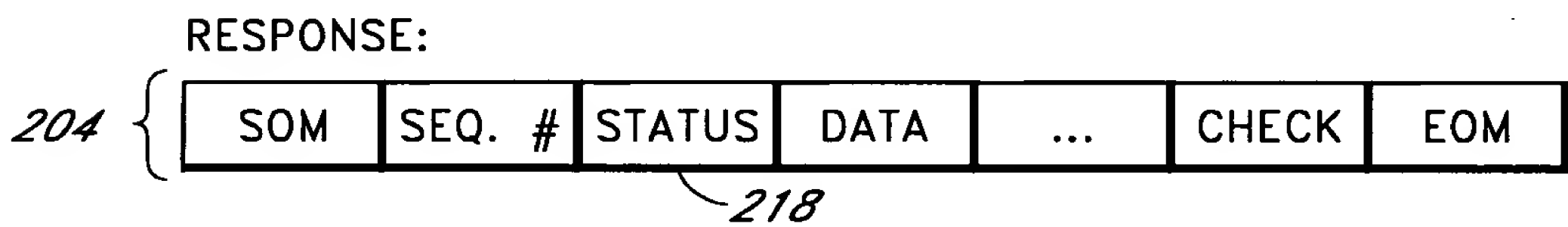
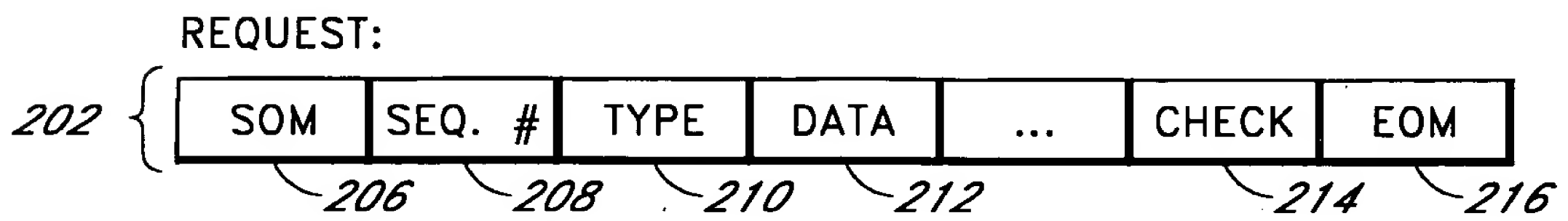
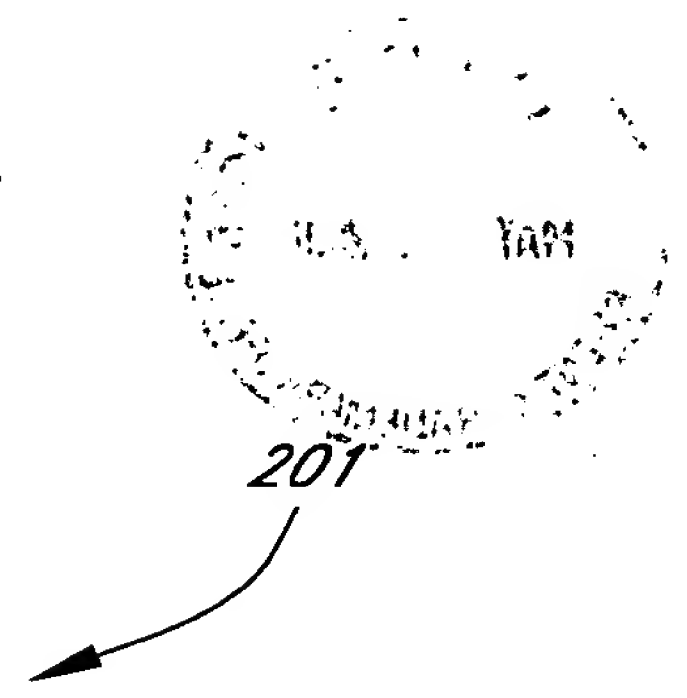


FIG. 3

# POWER-ON PROCESS

FIG. 4A

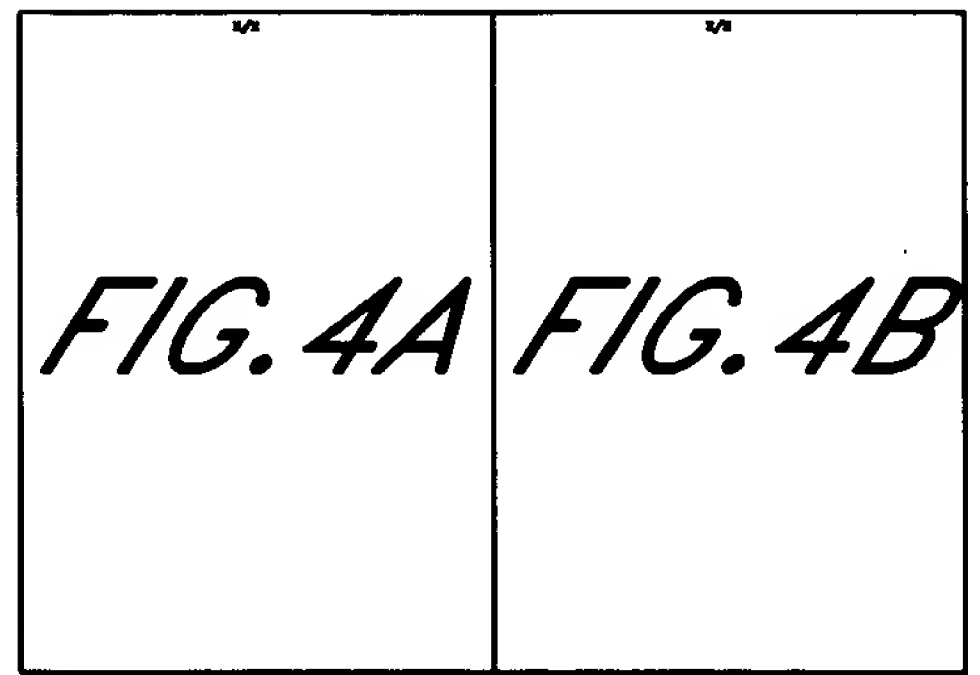
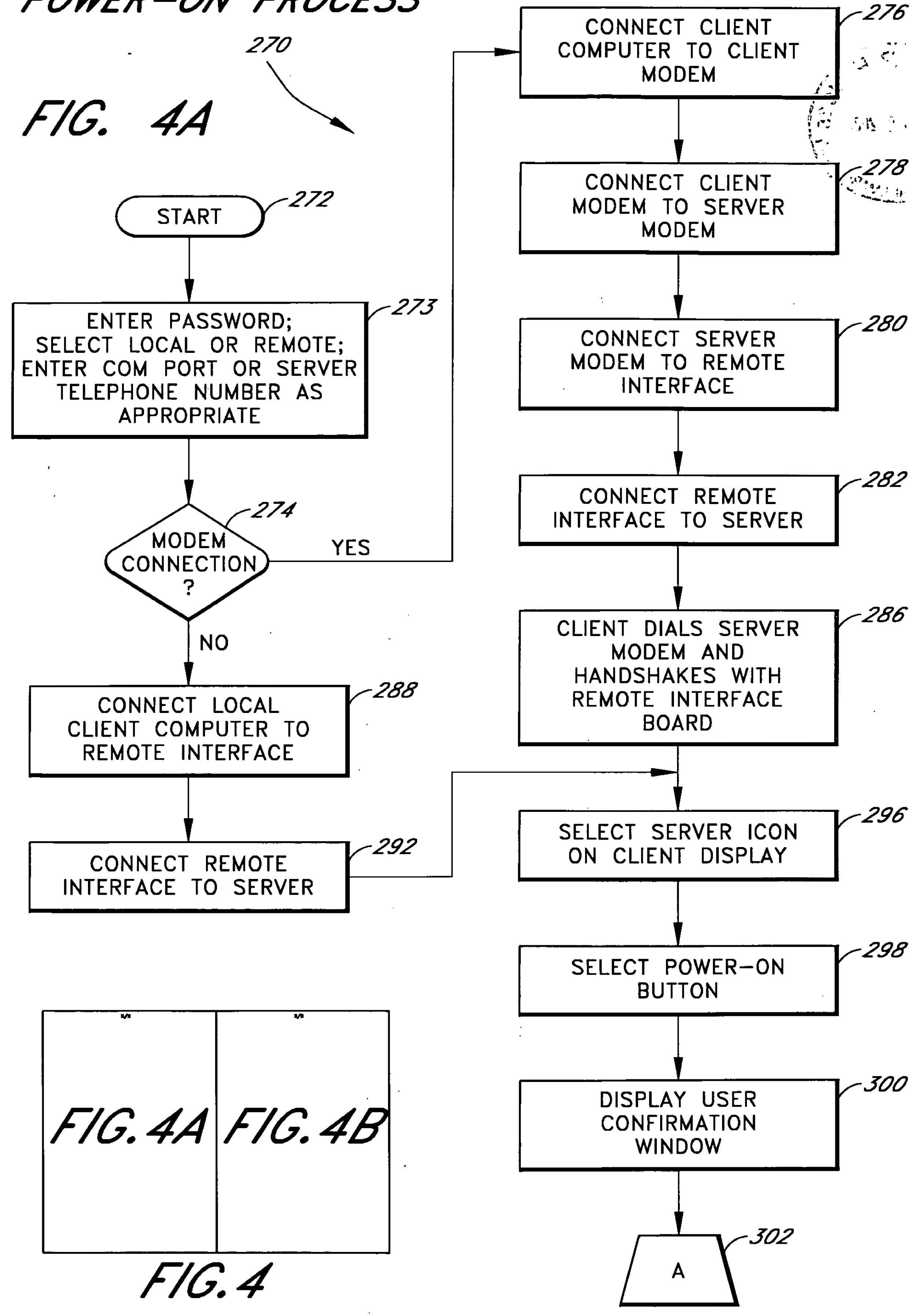


FIG. 4

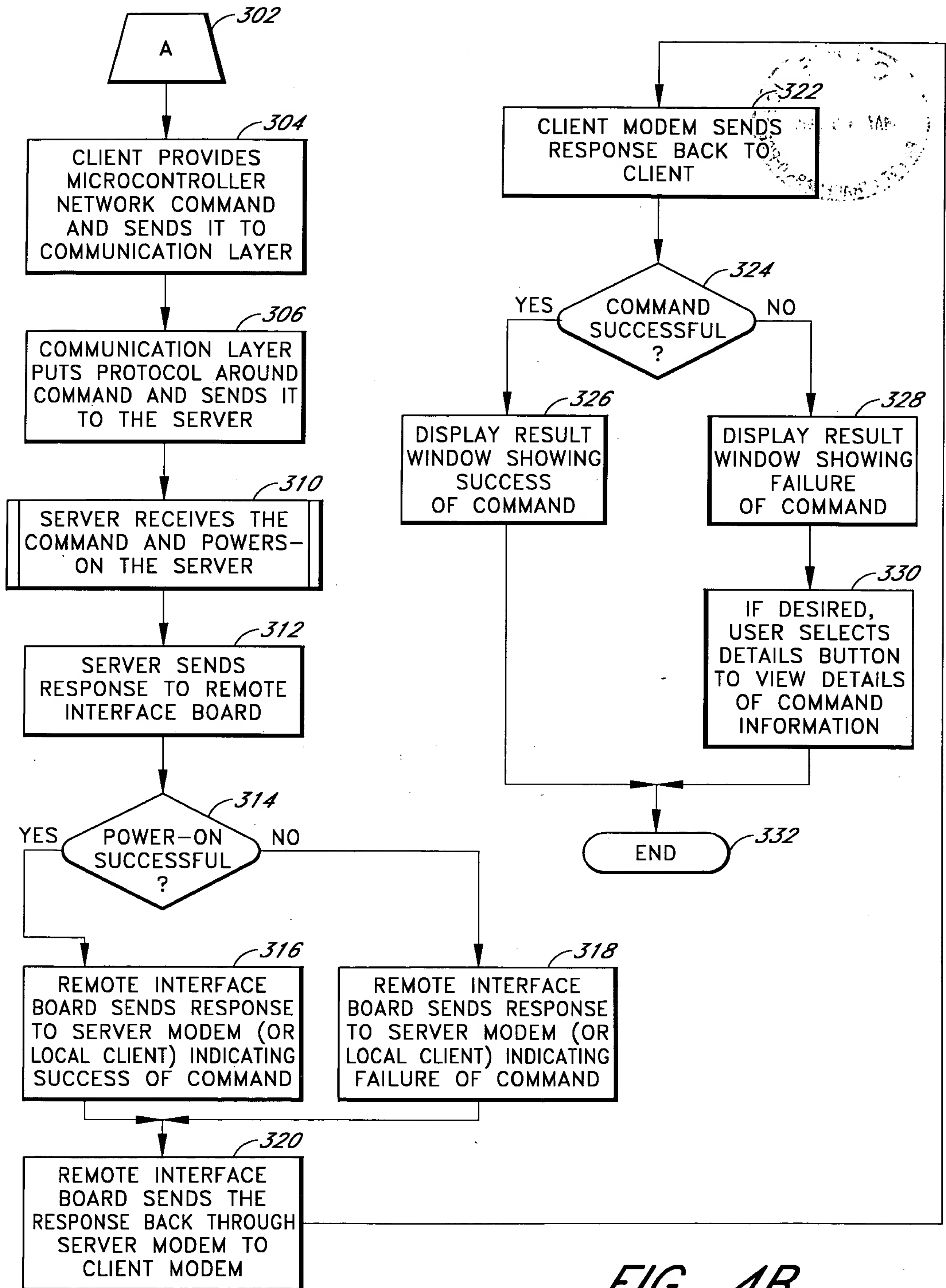


FIG. 4B

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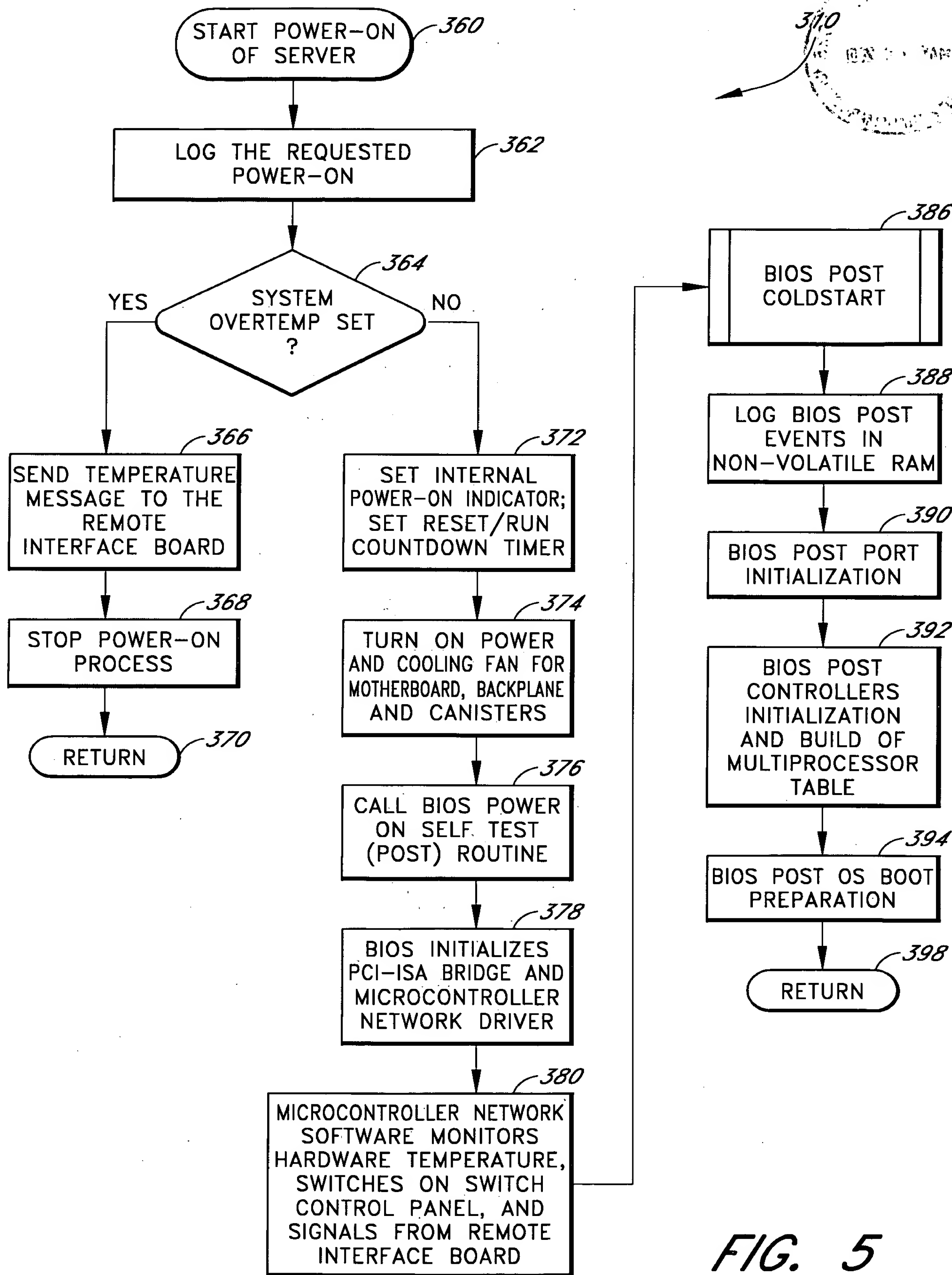


FIG. 5

# POWER-OFF PROCESS

FIG. 6A

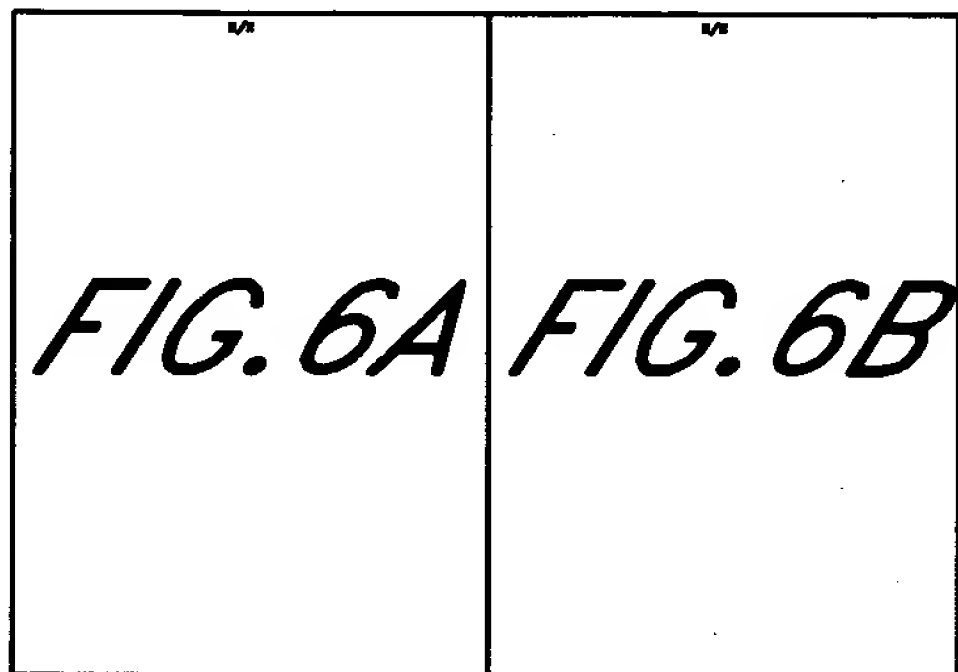
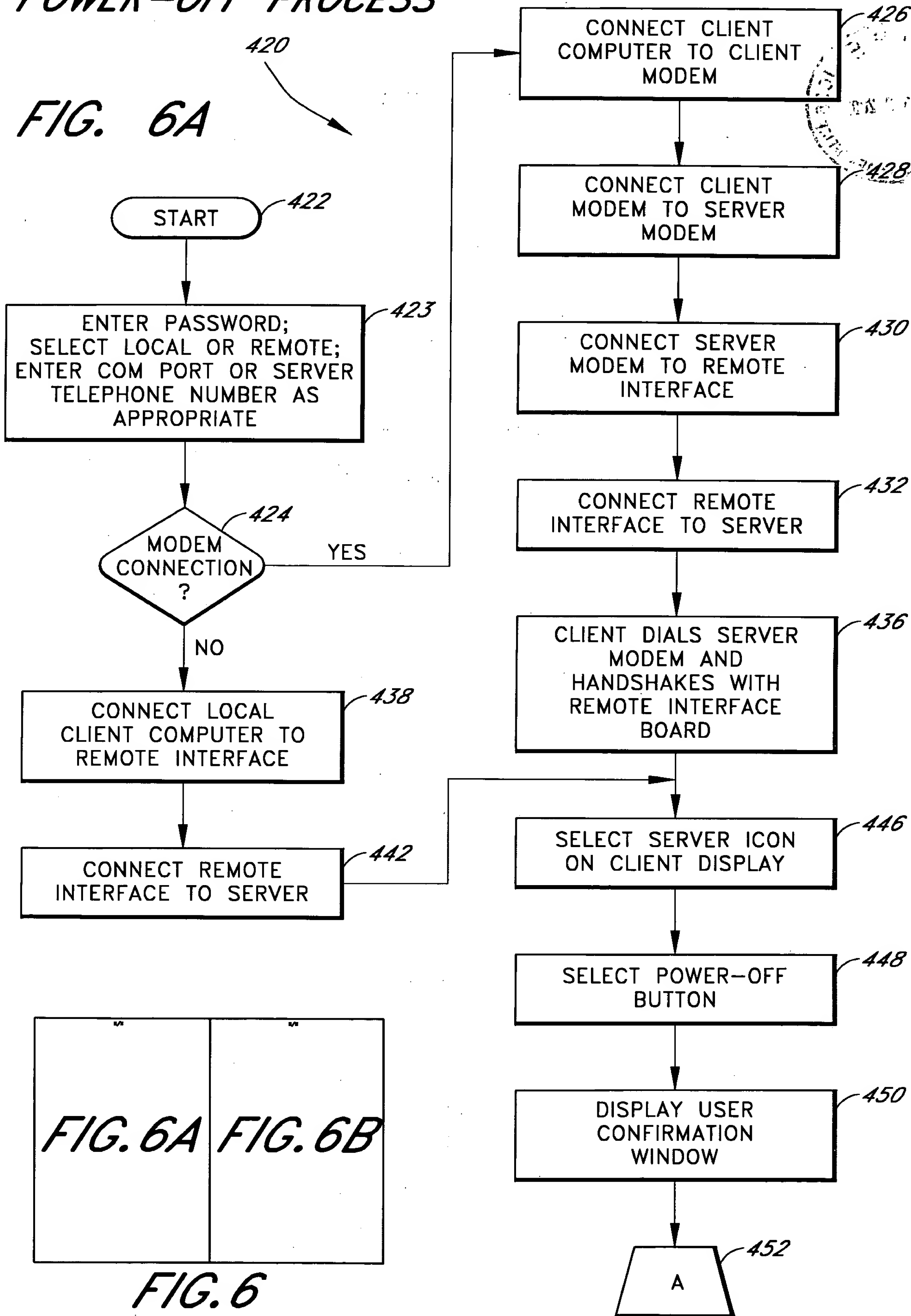


FIG. 6

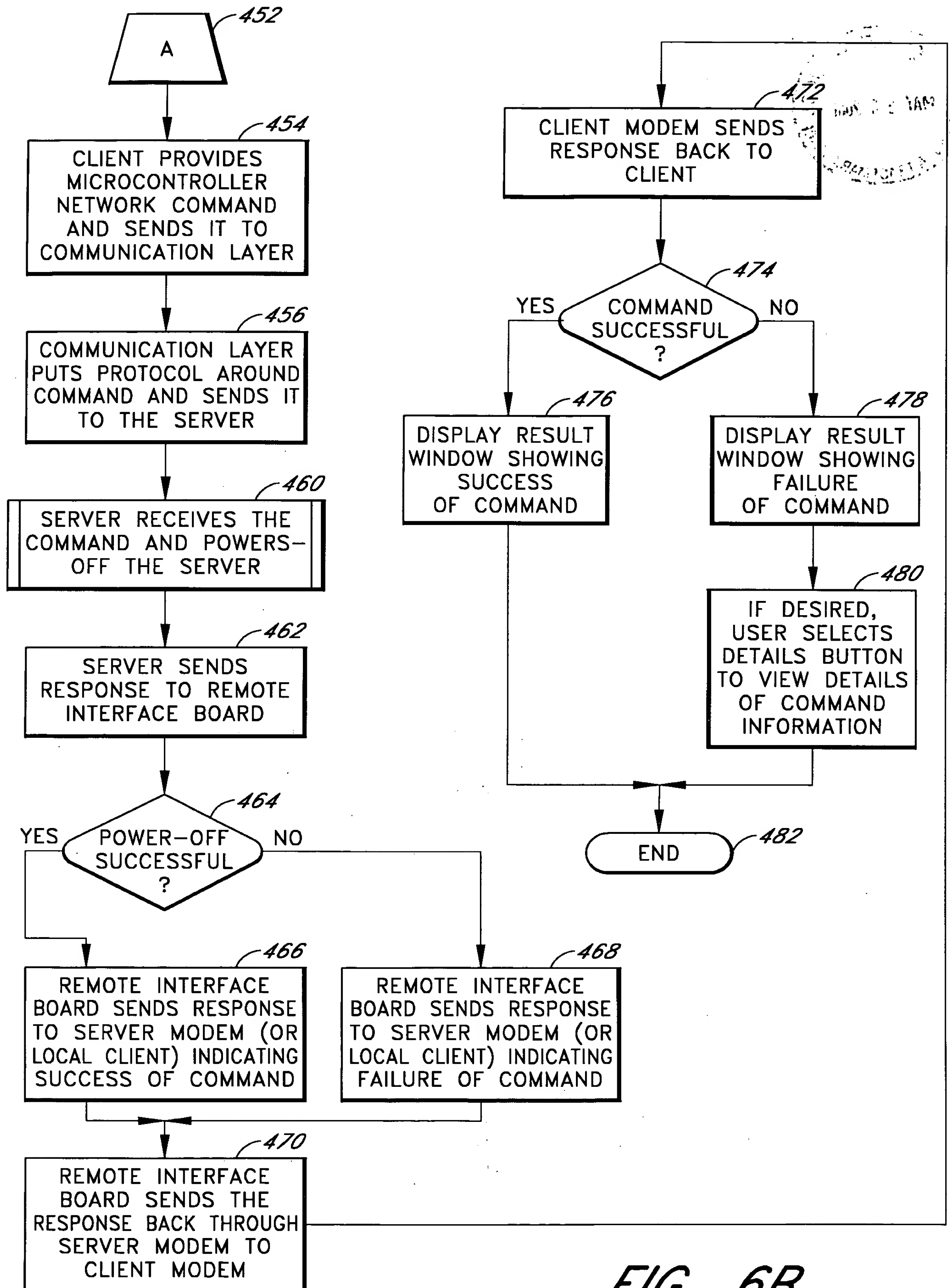


FIG. 6B



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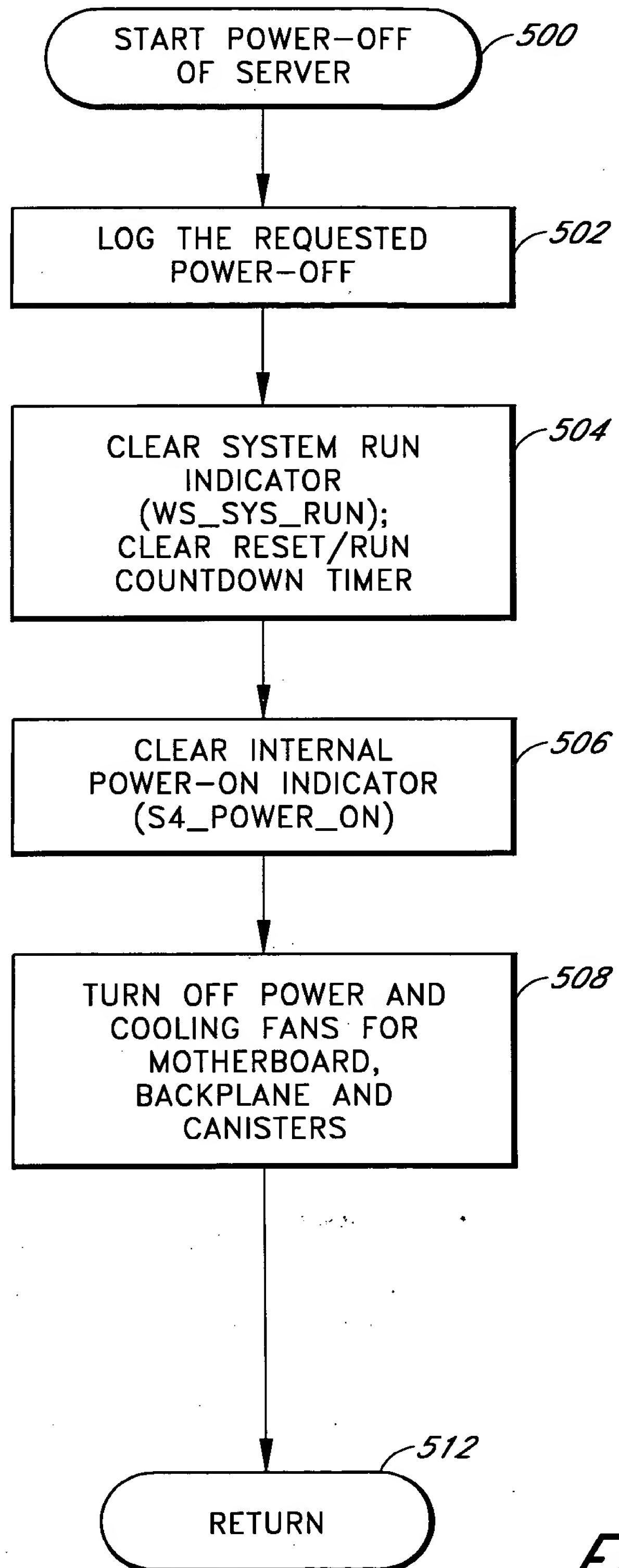
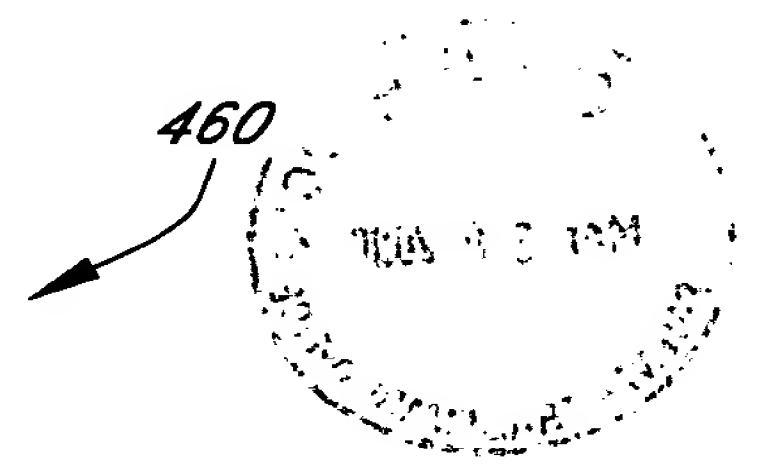


FIG. 7

# RESET PROCESS

FIG. 8A

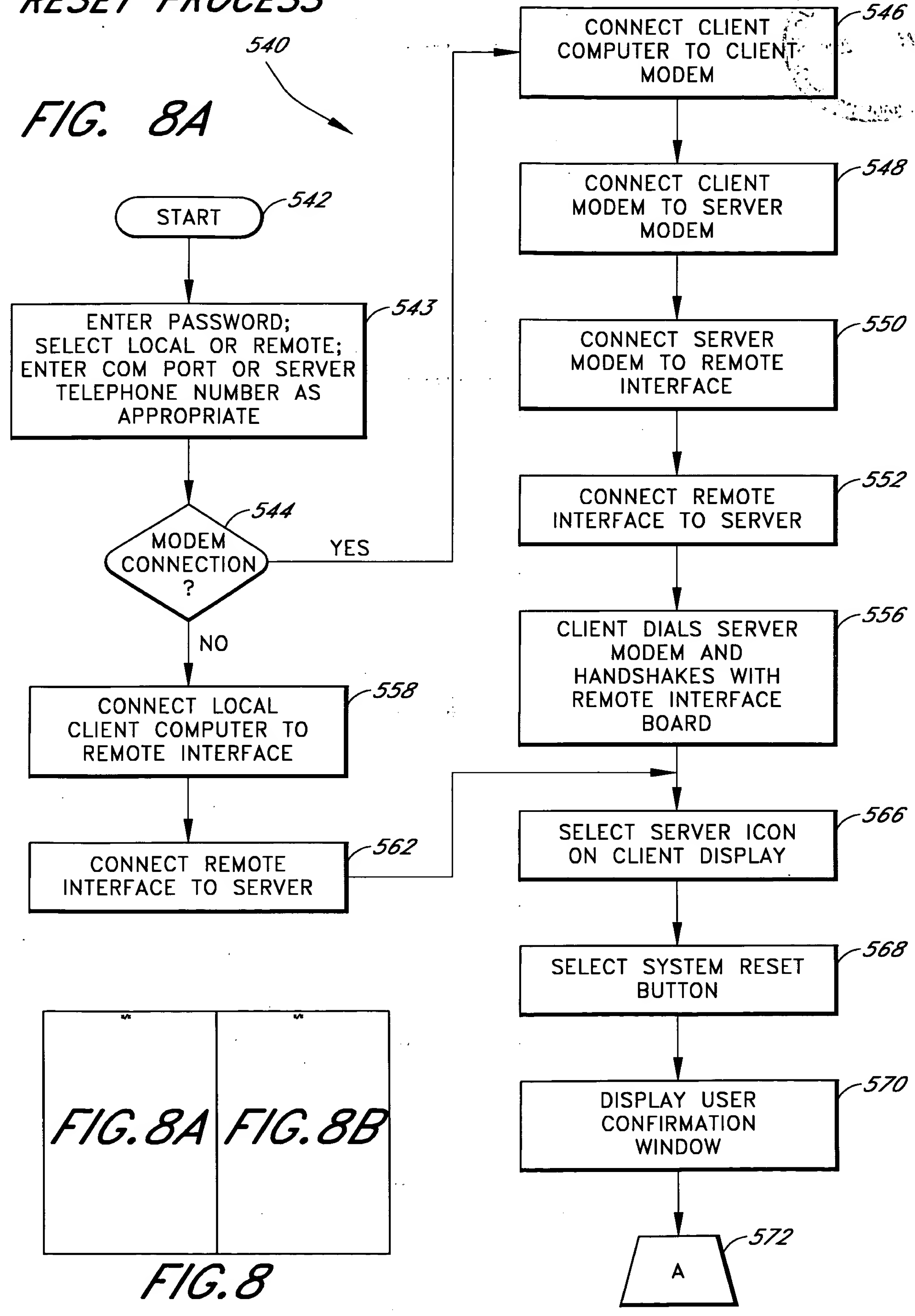


FIG. 8A FIG. 8B

FIG. 8

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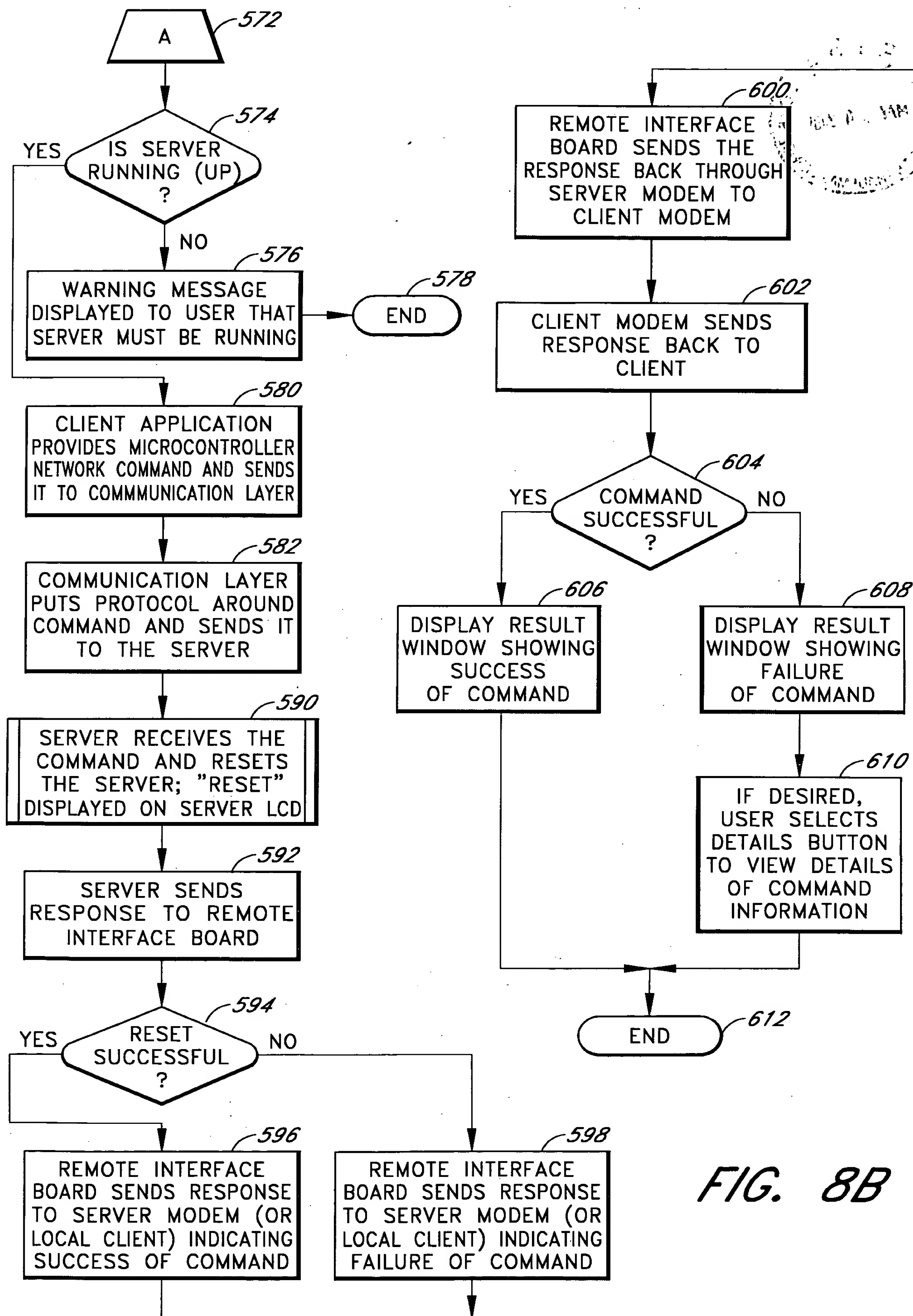


FIG. 8B

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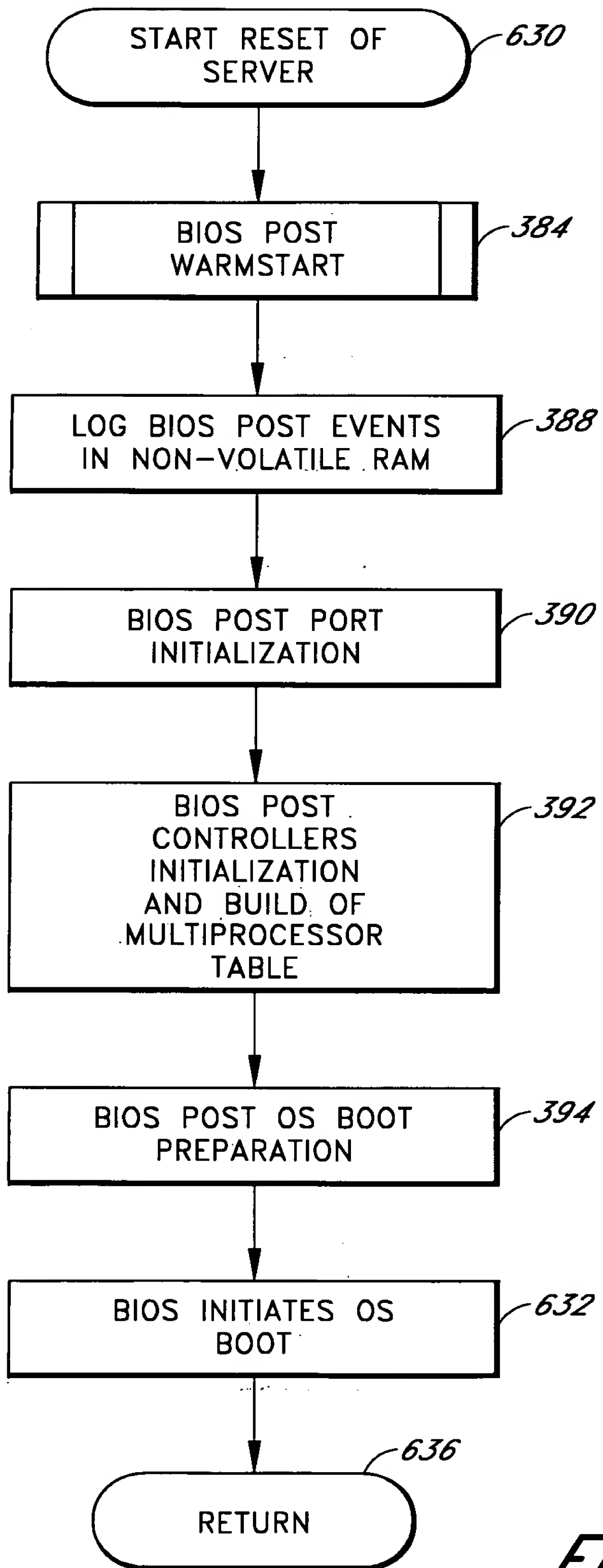
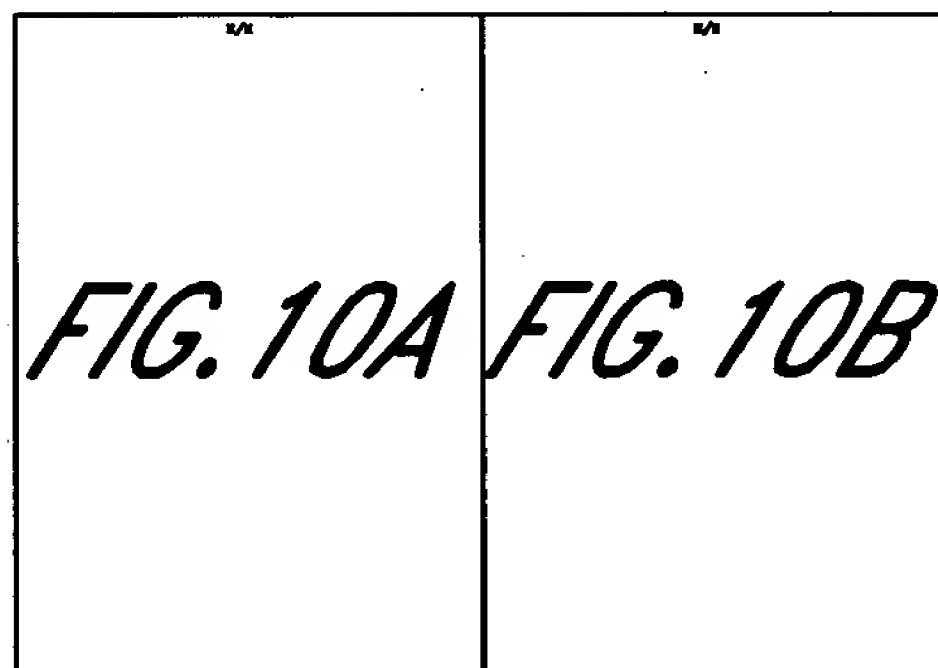
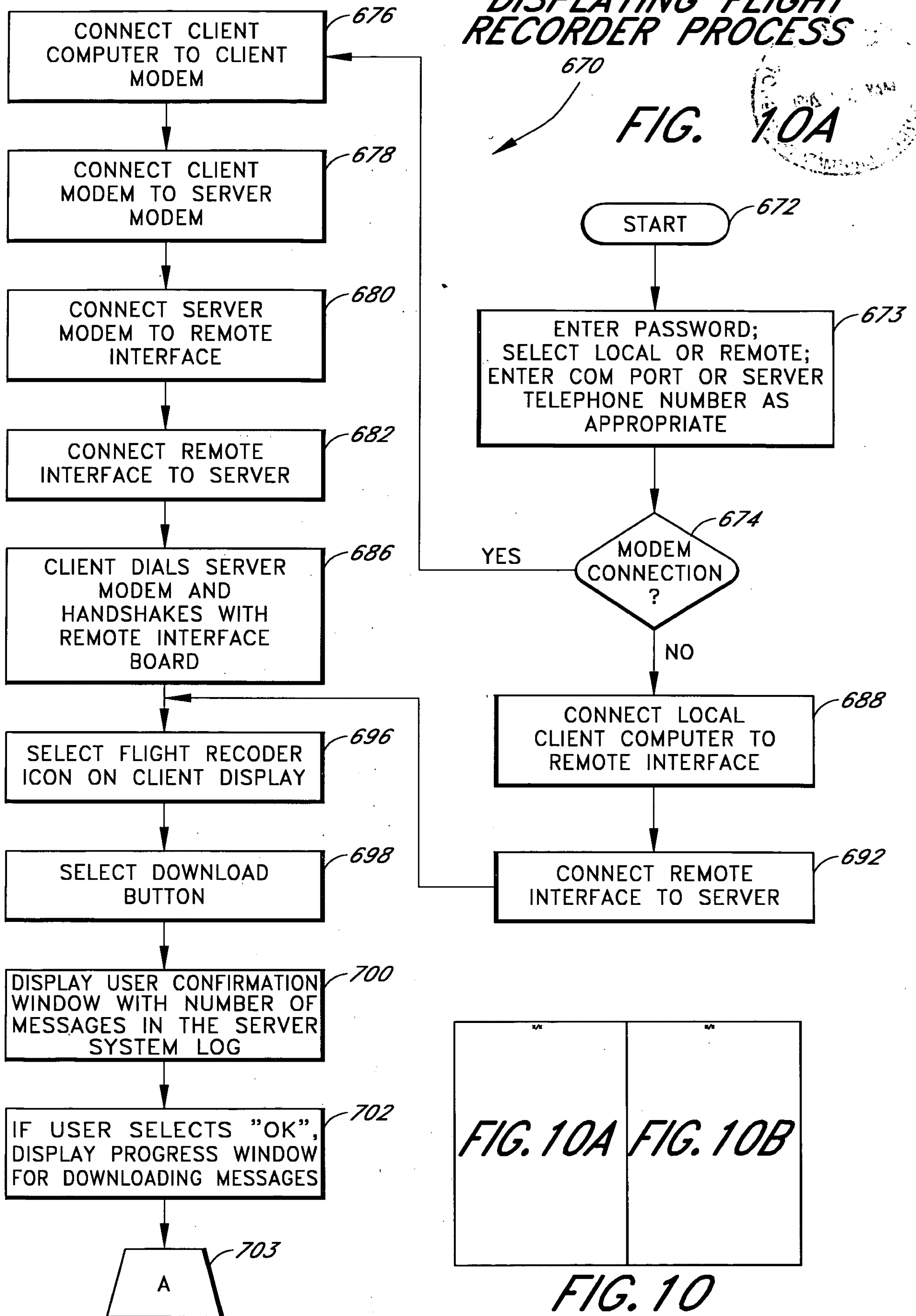


FIG. 9

# DISPLAYING FLIGHT RECORDER PROCESS

## FIG. 10A



## FIG. 10

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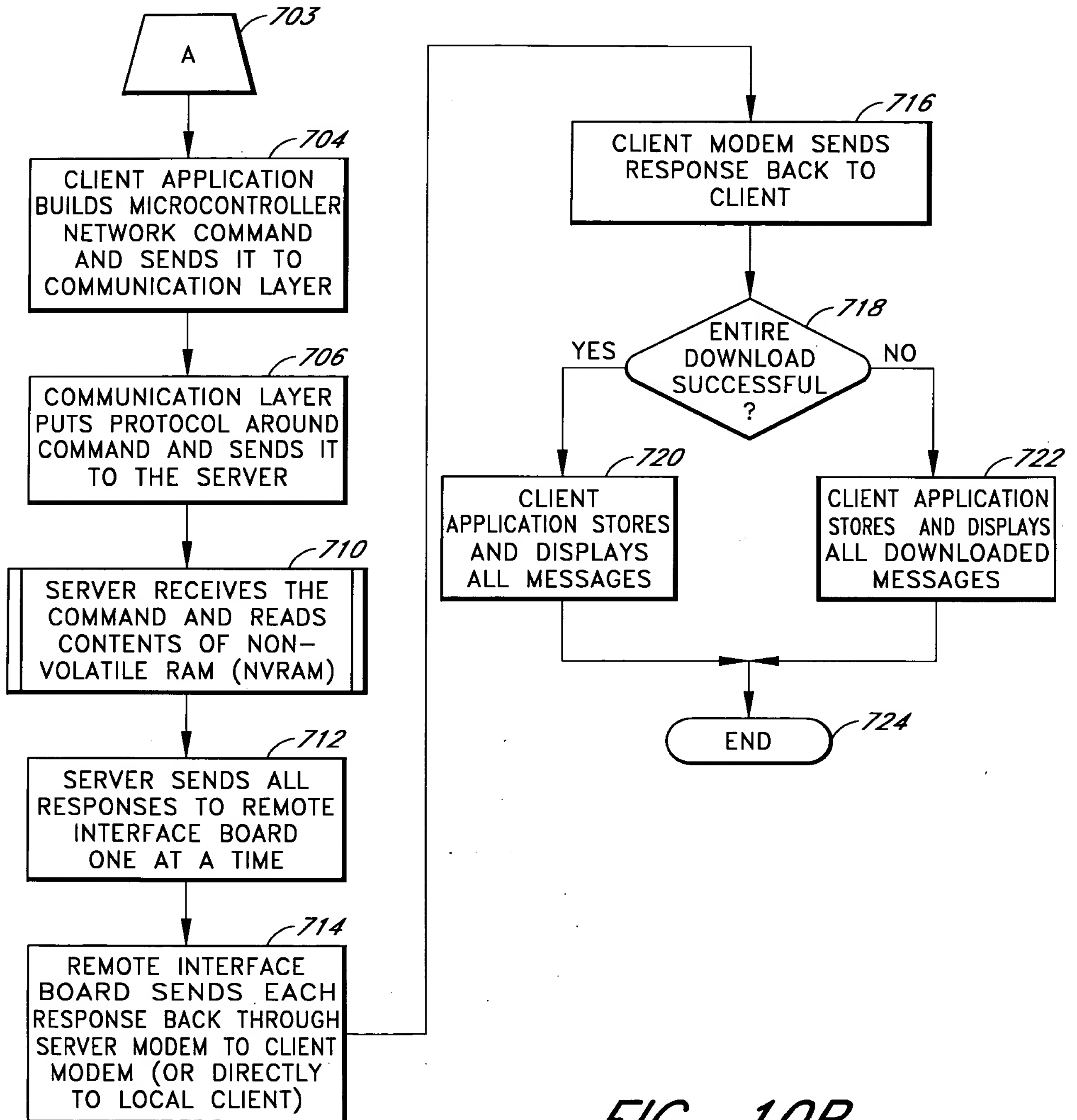


FIG. 10B

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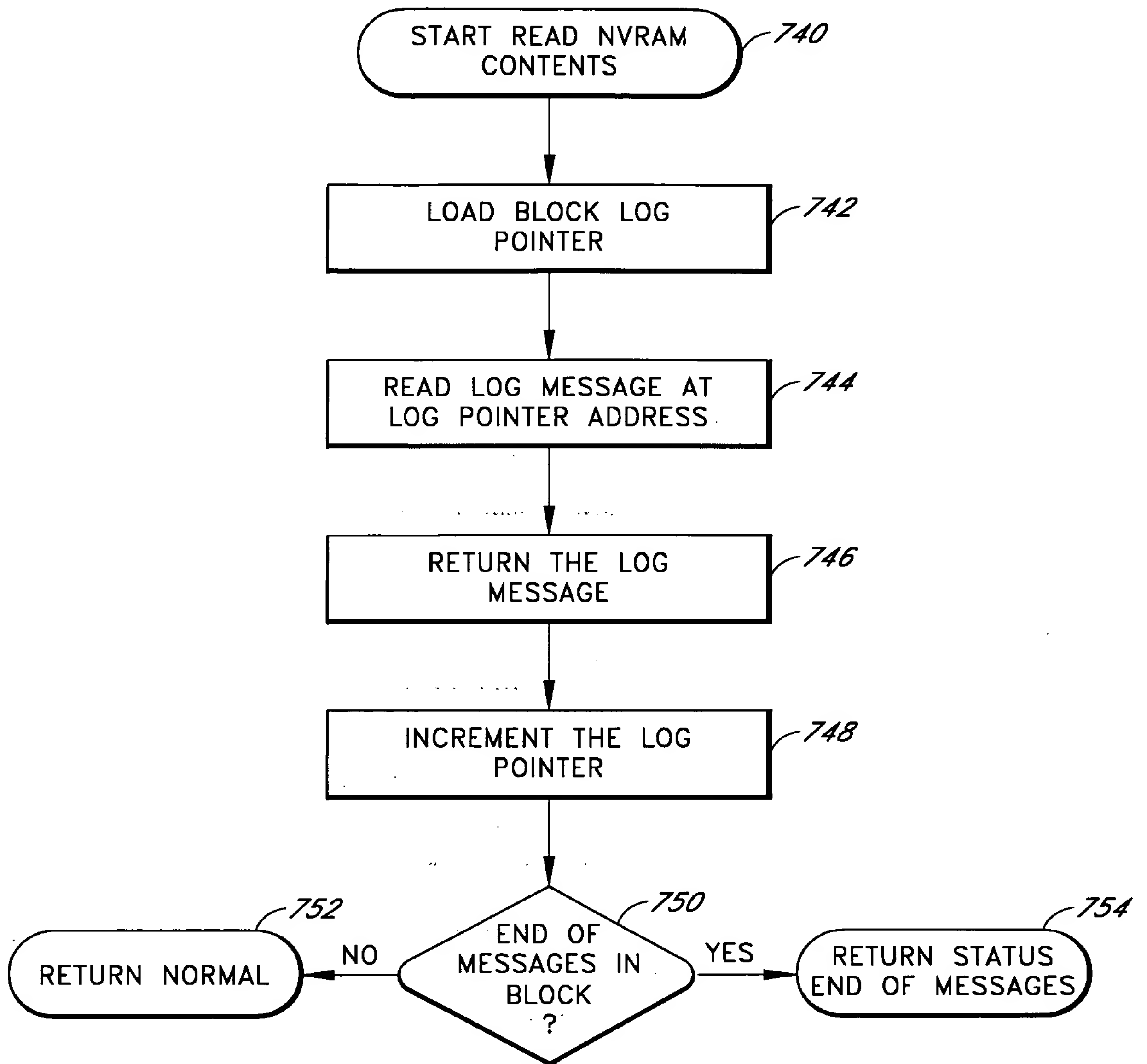
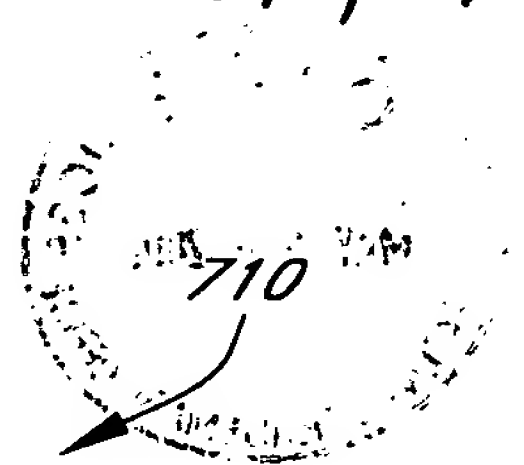


FIG. 11

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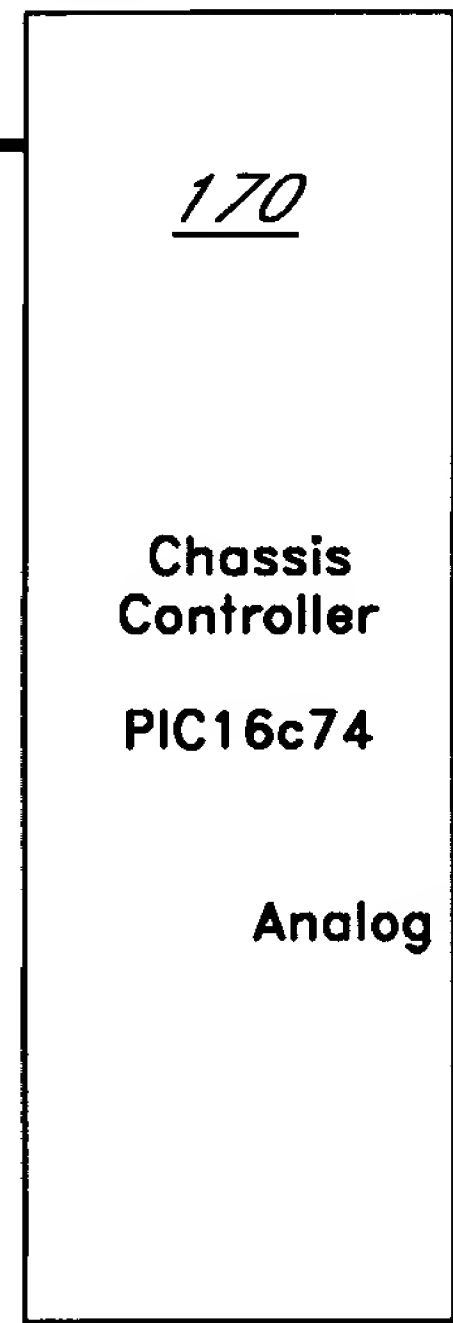
# Microcontroller Network Bus

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A

Backplane

Power from Bias +5v

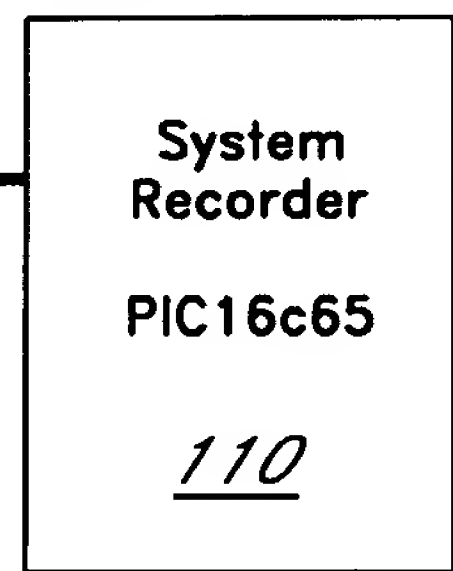


- Chassis Type Detection
- Canister SN Detection
- Motherboard SN Detection
- Backplane SN Detection
- PS Control/Monitoring
- PS On Switch
- PS On Signal
- PS SN Detection
- PS DC OKs
- PS AC OKs
- Power Voltage Level Detection
- +3v +5v +12v -12v VRef
- Power from Bias +5v
- Exhaust Temp (4/2 wire Detectors)

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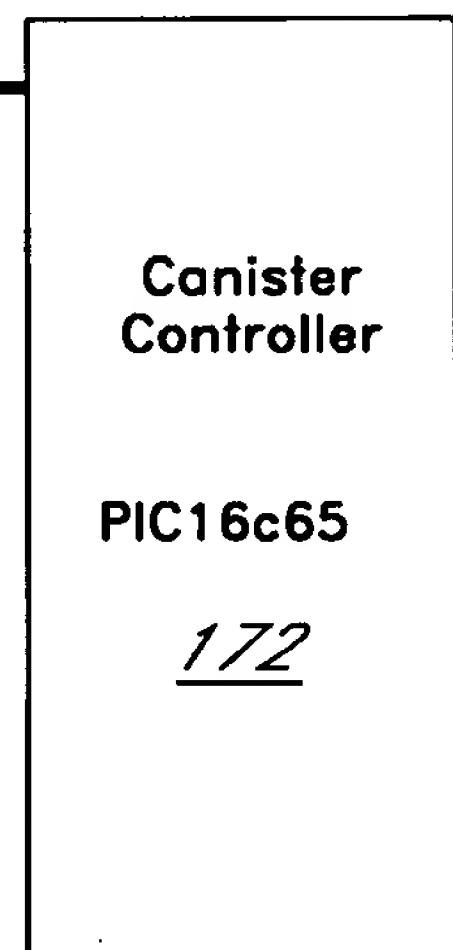
- Temperature Detector (2) On Backplane
- Temperature Detector (2) On Motherboard
- Ambient Temp (1/2 wire detector)
- Temperature Detector (2) On Motherboard

Power from Bias +5v



- Non Volatile RAM
- Address
- Data
- R/W/E
- 112
- Power from Bias +5v
- Timer Chip
- Reset
- Data
- Clock
- 760
- Power from Bias +5v

Canister Card



- Canister Address Detection
- I/O Processor NMI
- PCI Card Power Control
- JTAG Control
- FRU/Fan Fault LEDs (2)
- Fan Mux
- 767
- Fan Speed Detection
- 766
- Fan Speed Control
- PCI Ext Bd Detection
- I/O Processor SN Detection

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FIG. 12A



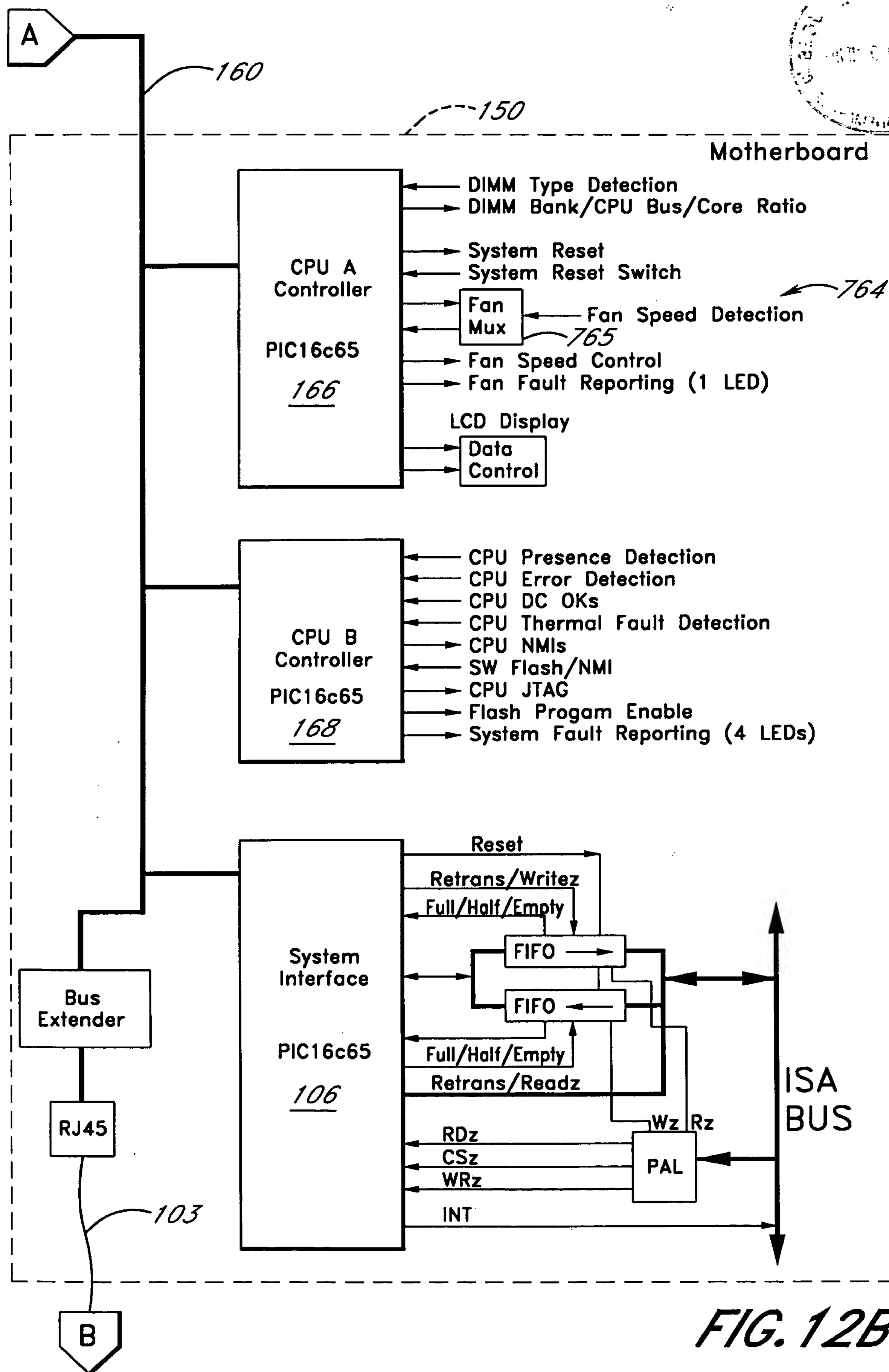


FIG. 12B

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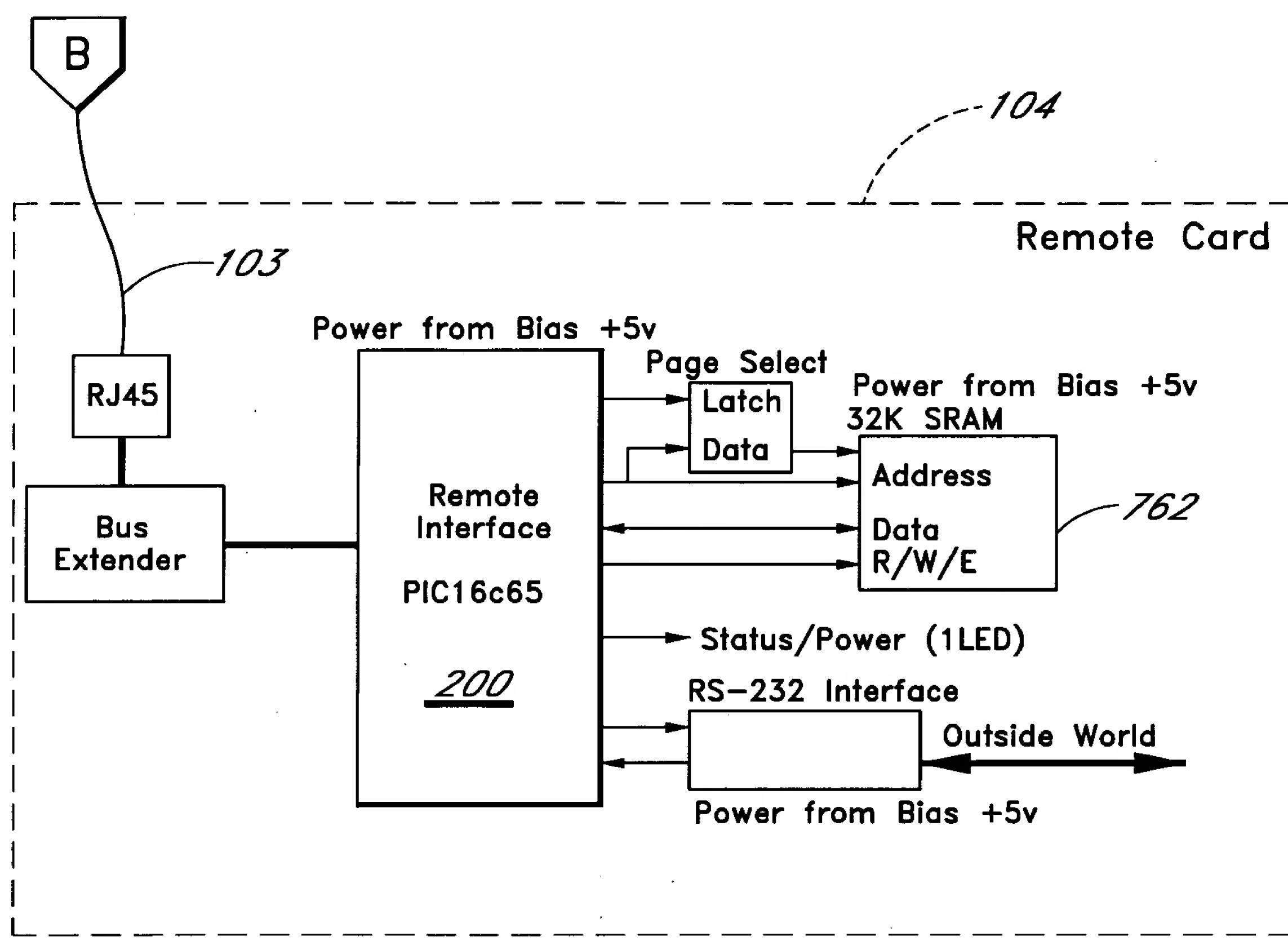
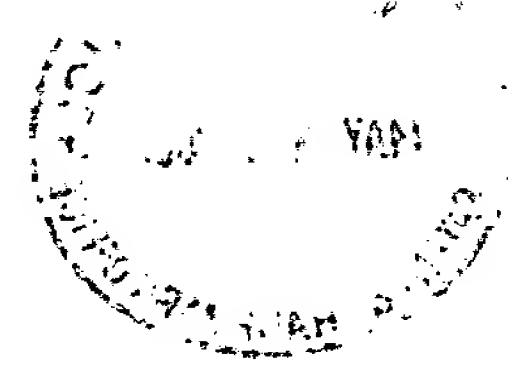
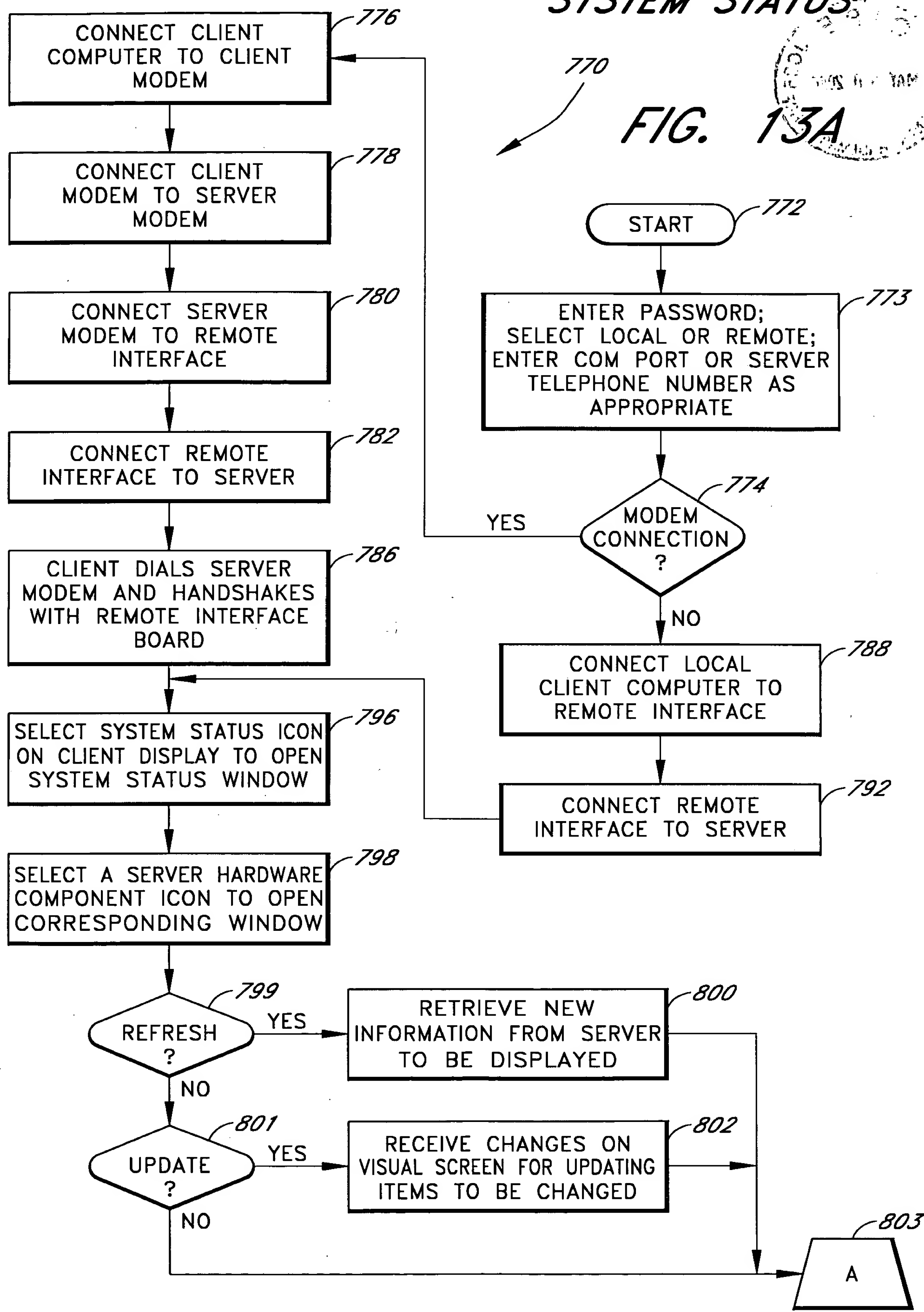


FIG. 12C

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# SYSTEM STATUS

FIG. 13A



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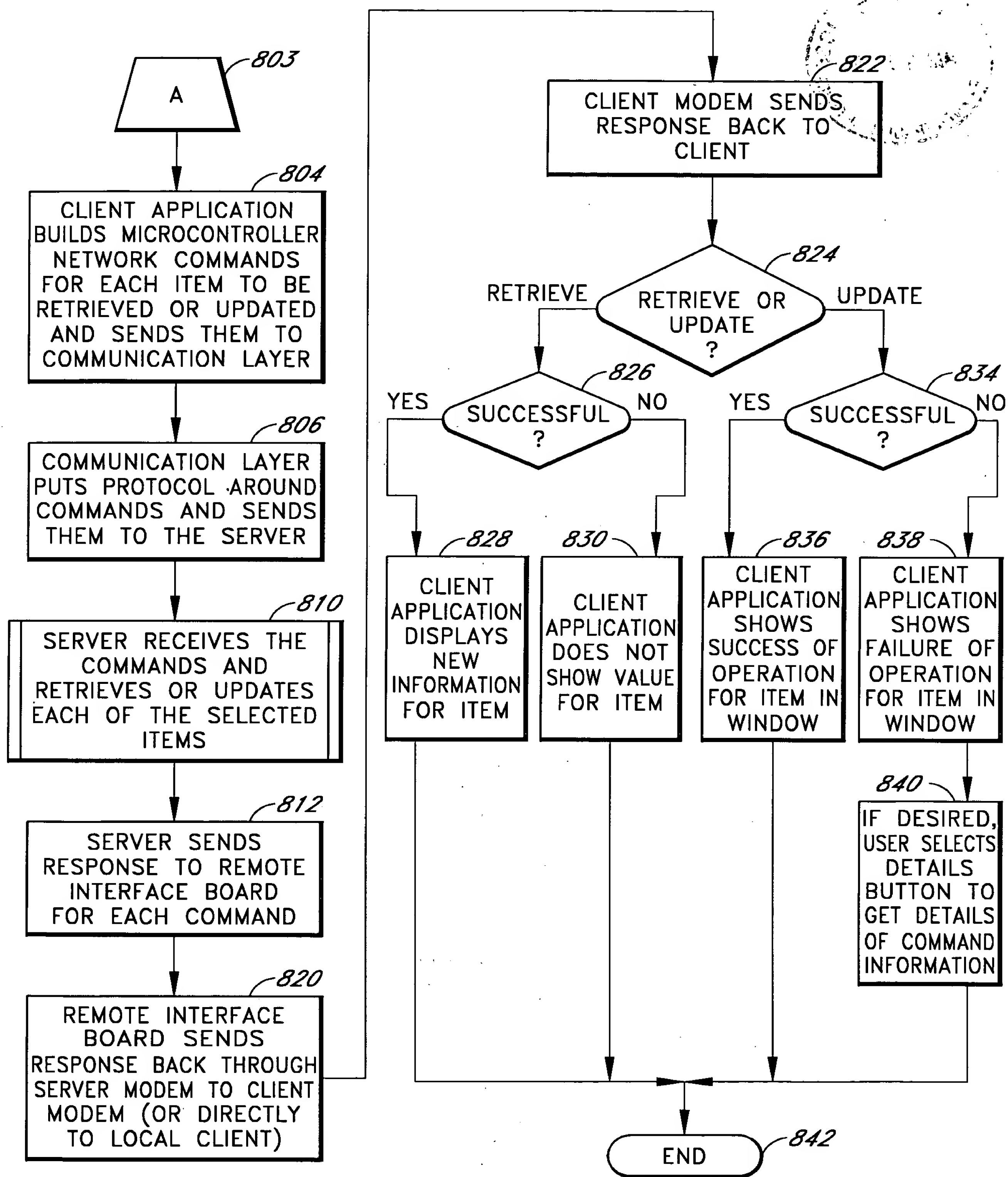


FIG. 13B

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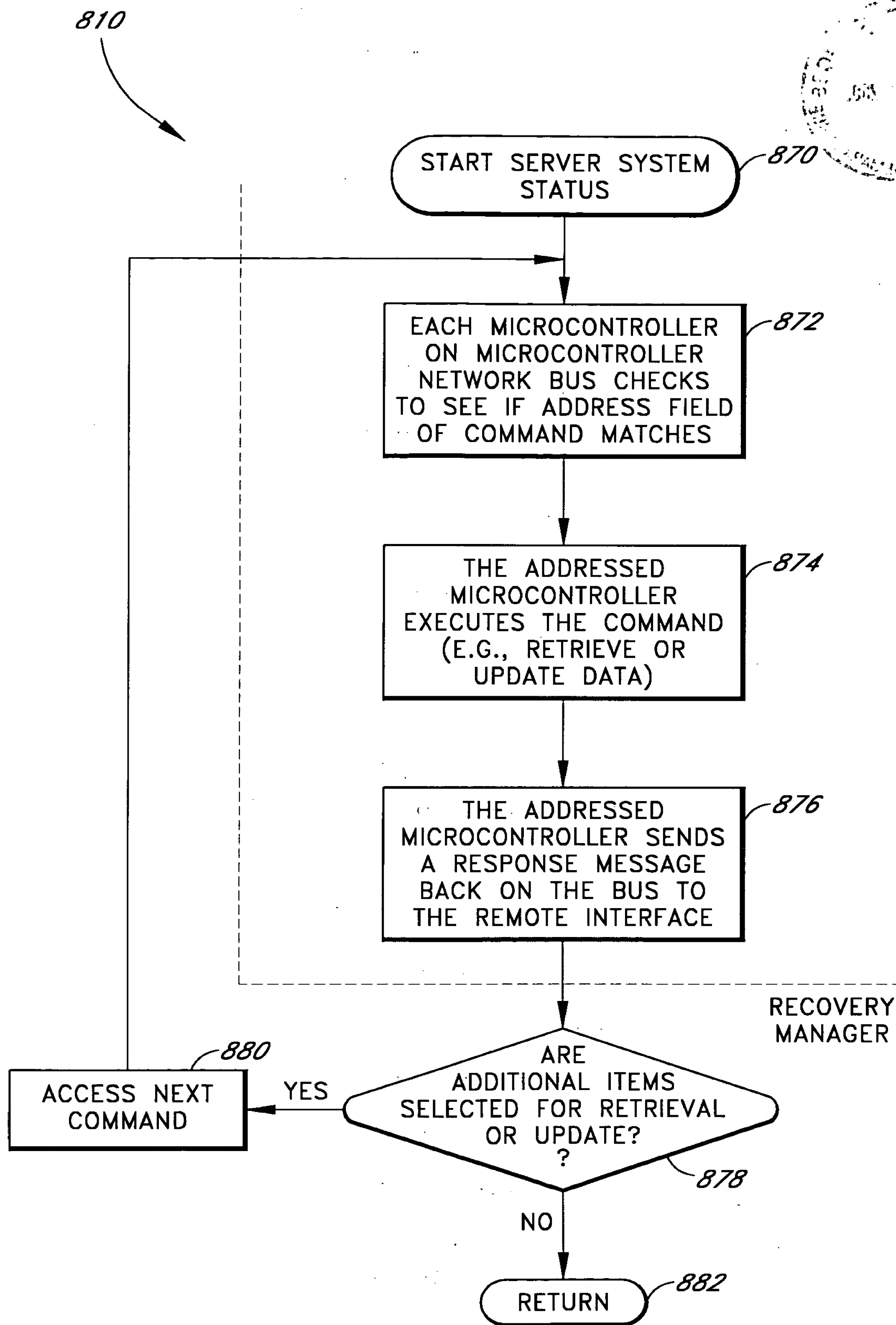


FIG. 14

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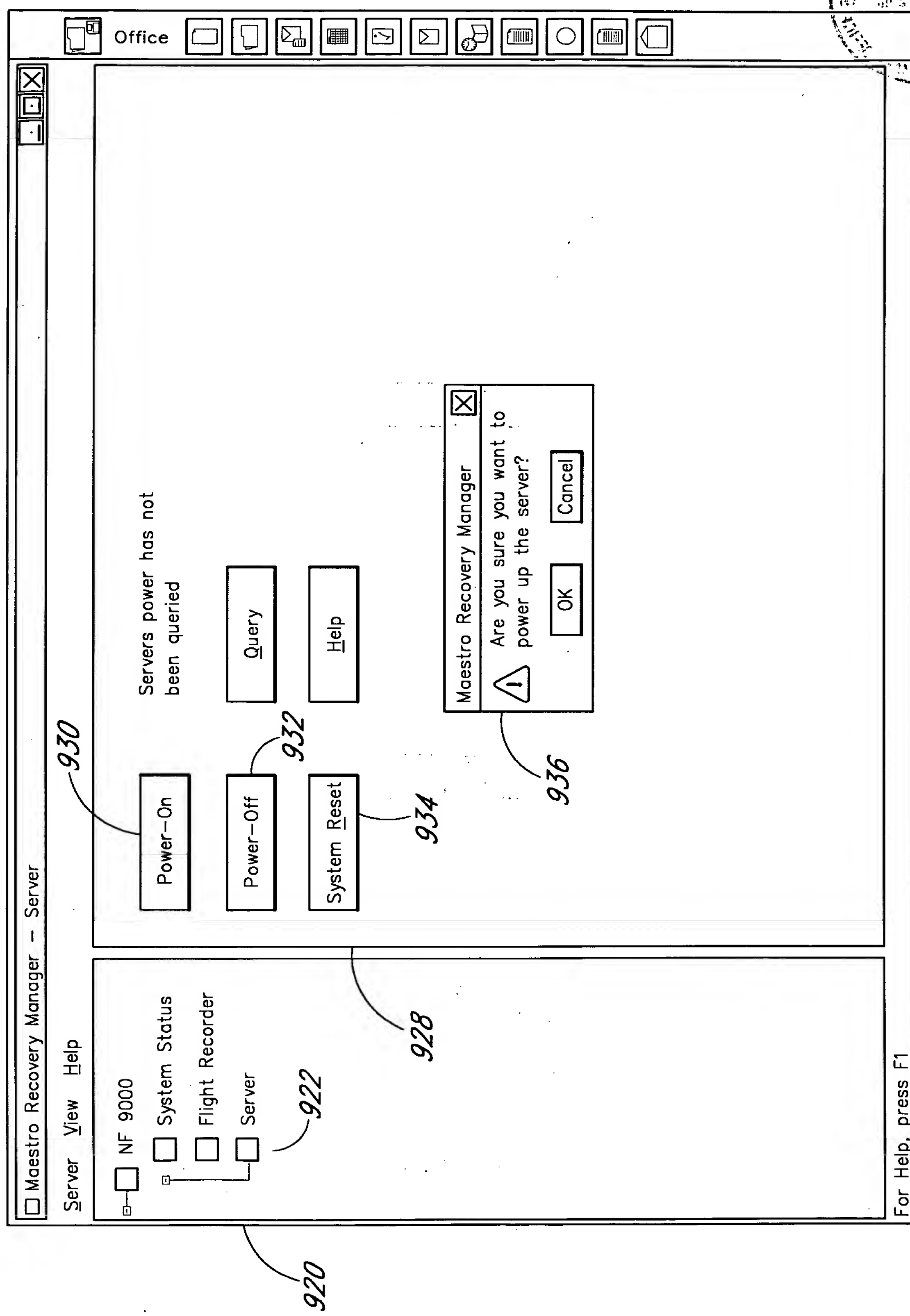
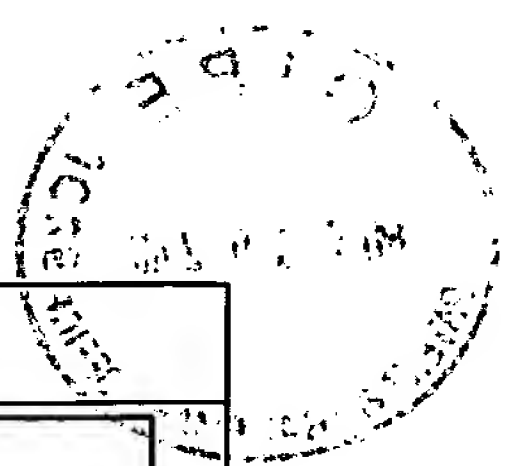


FIG. 15

For Help, press F1

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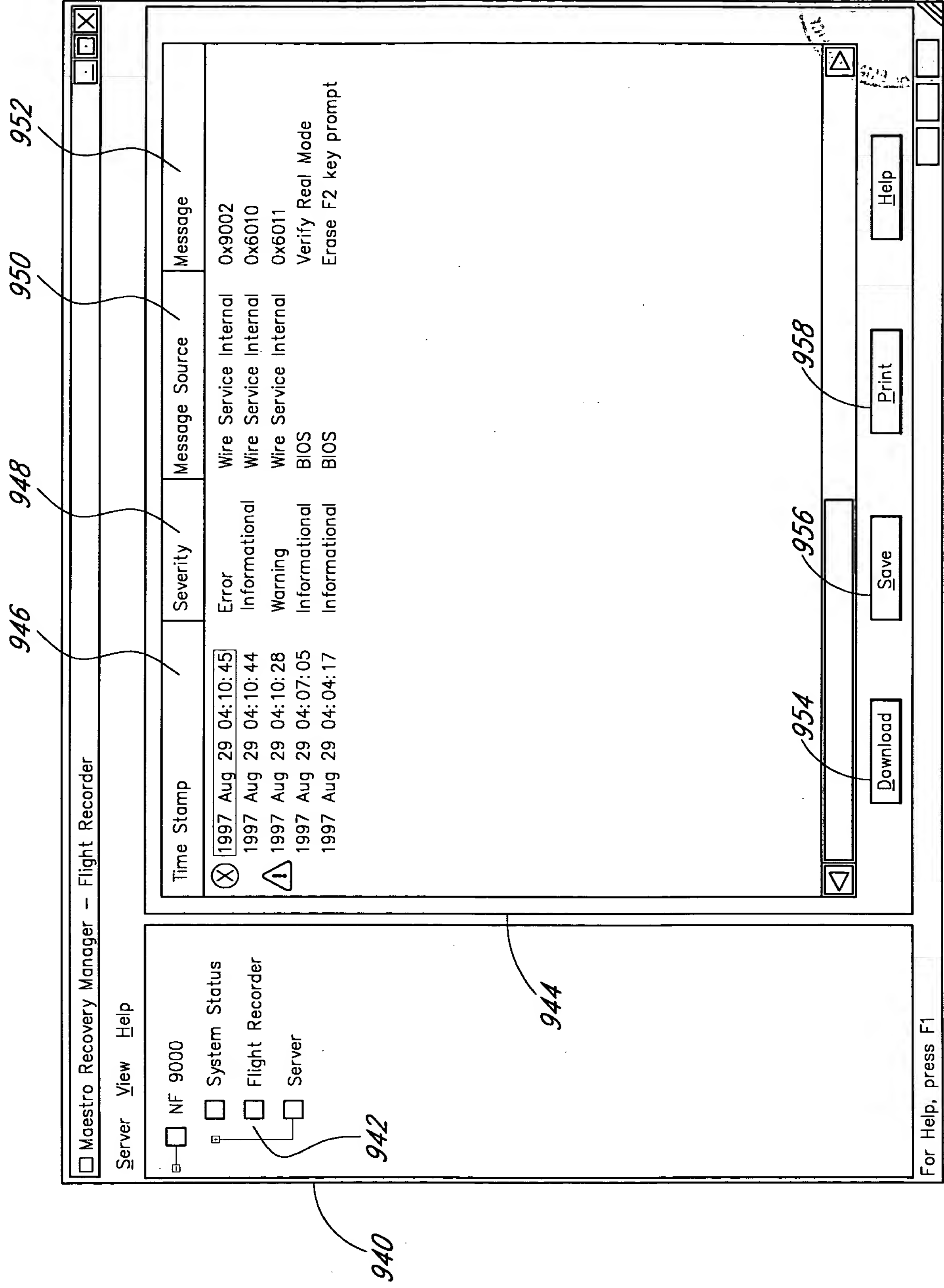


FIG. 16

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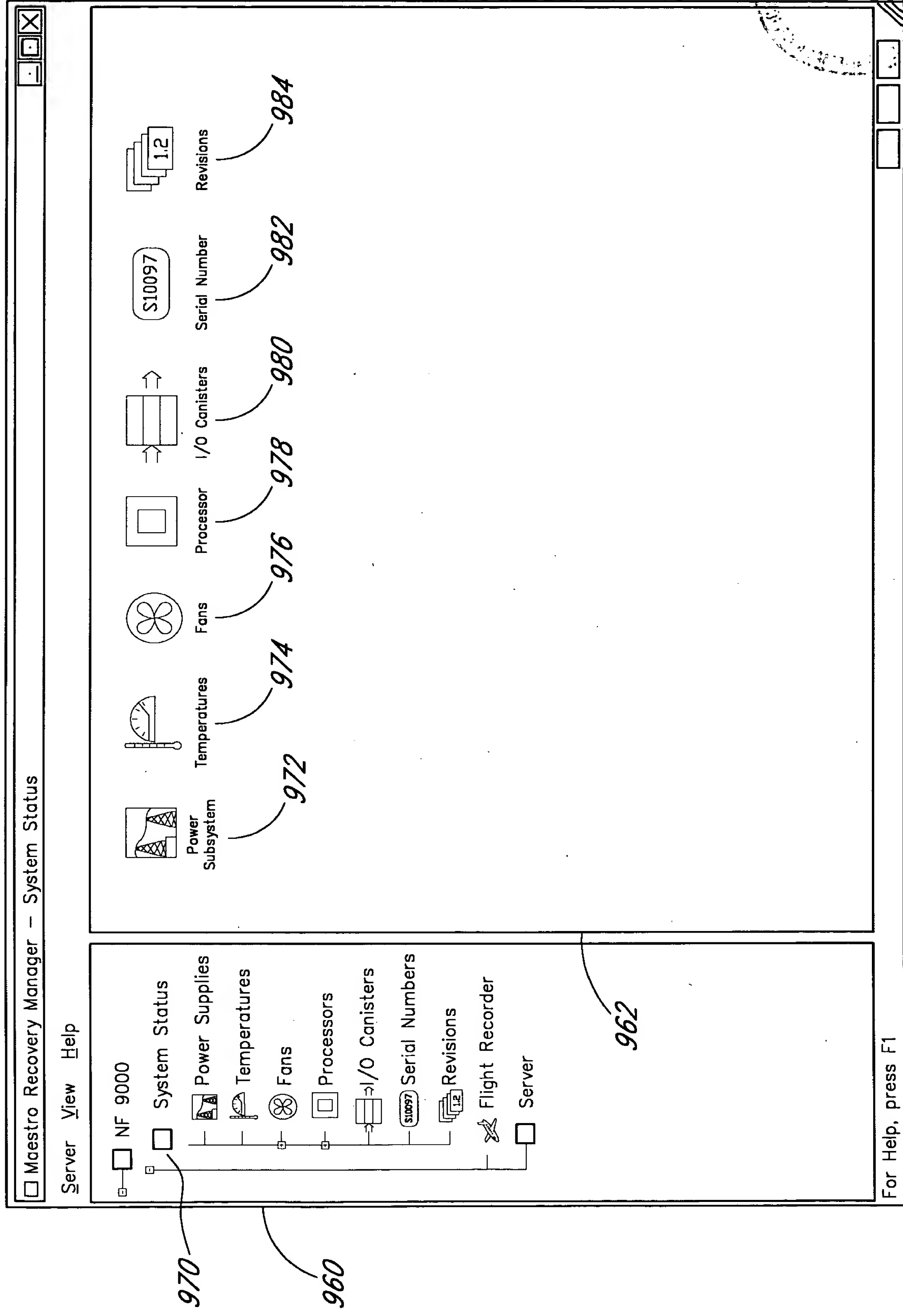


FIG. 17





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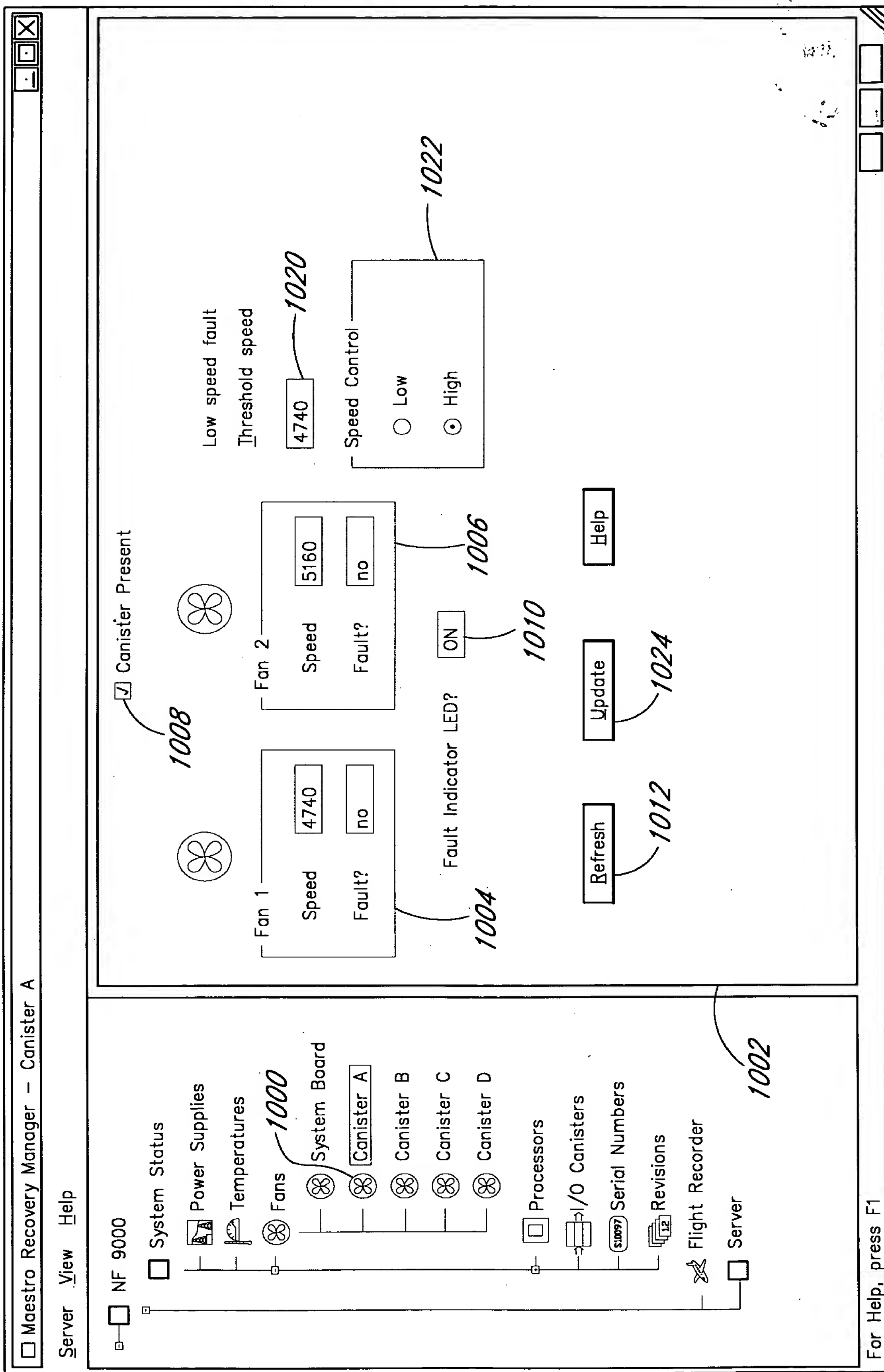


FIG. 19